TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

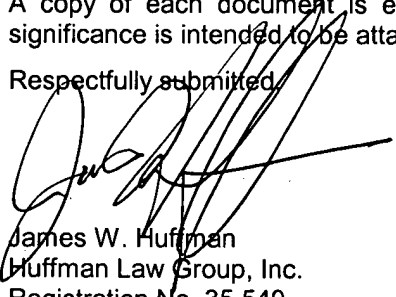| In re application of:	Rodney E. Hooker
Serial No.:	NA
Filed:	Herewith
Docket:	CNTR.2229
For:	MICROPROCESSOR AND APPARATUS FOR PERFORMING SPECULATIVE LOAD OPERATION FROM A STACK MEMORY CACHE

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.
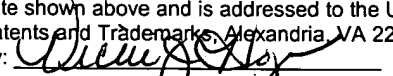
A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted

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Date: __1-15-04__

Substitute for form 1449A/PTO	**Complete if Known**	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	NA
	Filing Date	Herewith
	First Named Inventor	**Rodney E. Hooker**
	Group Art Unit	
	Examiner Name	
Sheet 1 of 3	Attorney Docket Number	CNTR.2229

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	U.S. Patent Document Number	Kind Code[2]	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	5043870		Ditzel et al	08/27/1991	
	AB	5953741		Evoy et al	09/14/1999	
	AC	6009499		Sailendra Koppala	12/28/1999	
	AD	6532531		O'Connor et al	03/11/2003	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	Foreign Patent Document Number[4]	Kind Code[5]	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

Examiner Signature		Date Considered	

Substitute for form 1449A/PTO **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** (use as many sheets as necessary			***Complete if Known***	
			Application Number	NA
			Filing Date	**Herewith**
			First Named Inventor	**Rodney E. Hooker**
			Group Art Unit	
			Examiner Name	
Sheet	2	of	2	Attorney Docket Number	CNTR.2229

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	AA	(in the file SCreport.pdf): Implementing a Stack Cache; Hensath, Morton, Sjodin; obtained from http://www.owlnet.rice.edu/~elec525/projects/SCreport.pdf; accessed on 8-13-2003.	
	AB	(in the file JavaStack Cache.doc); Design Issues – Stack Cache; obtained from http://murray.newcastle.edu.au/users/students/1999/c9510422/design1.html; http://murray.newcastle.edu.au/users/students/1999/c9510422/design2.html; http://murray.newcastle.edu.au/users/students/1999/c9510422/design3.html; http://murray.newcastle.edu.au/users/students/1999/c9510422/design4.html; http://murray.newcastle.edu.au/users/students/1999/c9510422/architecture.html; accessed on 12-02-2003.	
	AC	(in the fine AMD Hammer micro architecture.pdf): AMD's Hammer micro architecture; http://chip-architect.com/news/hammer.jpg; accessed on 08-29-2002.	

Examiner Signature		Date Considered	